

AMENDMENTS TO THE SPECIFICATION

Page 1, please delete line 10 and substitute therefor the following:

DESCRIPTION OF THE RELATED ARTS ART

Page 4, please delete lines 1 and 2:

~~{DISCLOSURE OF THE INVENTION}~~

~~{PROBLEMS TO BE SOLVED BY THE INVENTION}~~

Page 4, please delete line 19 and substitute therefor the following:

~~{MEANS FOR SOLVING THE PROBLEMS}~~ SUMMARY OF THE
INVENTION

Page 5, please delete paragraph [0012] and substitute therefor the following:

In addition, ~~since~~ because the first substrate also includes a transmitter circuit for outputting signals to the first coil when transmission digital data changes, power consumption can be reduced.

Page 5, please delete paragraph [0013] and substitute therefor the following:

Also, ~~since~~ because the second substrate further includes a receiver circuit that connects both ends of the second coil to a predetermined voltage source via resistors, the center voltage of voltage amplitude occurring at both ends of the receiver coil when receiving signals can be made into a voltage value optimal for the signal amplitude.

Page 5, please delete paragraph [0014] and substitute therefor the following:

Further, ~~since~~ because the first coil is inductively coupled to the second coils of a plurality of second substrates, a bus over three or more substrates can be formed.

Page 5, please delete paragraph [0015] and substitute therefor the following:

Still further, ~~since~~ because the second substrate also includes a receiver circuit that receives signals only for a predetermined cyclic period, it is possible to increase the SN ratio.

Page 6, please delete line 1:

~~{EFFECT OF THE INVENTION}~~

Page 6, please delete paragraph [0017] and substitute therefor the following:

Also, ~~since~~ because communications are carried out by current drive, the electronic circuit according to the present invention can be favorably employed for LSIs, and the like in which low voltage drive is highly demanded.

The present specification includes the contents described in the specification and/or the drawings of Japanese Patent Application No. 2004-037242 which is the basis of priority of the present application.

Page 14, please delete paragraph [0026] and substitute therefor the following:

Voltages shown in the [first LSI chip receiver coil voltage] and the [second LSI chip receiver coil voltage] are generated in the receiver coil L2 disposed on the first and the second LSI chips 11a and 11b by changes in the current at points B and C of the [transmitter coil current]. The center voltage of the voltage variation is V_{bias}. ~~Since~~ Because the first LSI chip 11a is farther from the third LSI chip 11c than the second LSI chip 11b, the voltage generated is lowered. The changes in voltage are amplified by the latched differential amplifier and the value is retained by latching, wherein digital data shown at point B of the [first and second LSI chip Rxdata] are brought about.